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<u>L32</u>	L26 and l10	8	<u>L32</u>
<u>L31</u>	L26 and l9	22	<u>L31</u>
<u>L30</u>	L26 and l8	0	<u>L30</u>
<u>L29</u>	L26 and l7	14	<u>L29</u>
<u>L28</u>	L26 and l6	2	<u>L28</u>
<u>L27</u>	L26 and l5	41	<u>L27</u>
<u>L26</u>	l3 and emulat?	198	<u>L26</u>
<u>L25</u>	l4 and l10	1	<u>L25</u>
<u>L24</u>	l4 and l9	1	<u>L24</u>
<u>L23</u>	l4 and l8	0	<u>L23</u>
<u>L22</u>	l4 and l7	0	<u>L22</u>
<u>L21</u>	l4 and l6	0	<u>L21</u>
<u>L20</u>	l4 and l5	4	<u>L20</u>
<u>L19</u>	l11 and l10	2	<u>L19</u>
<u>L18</u>	l11 and l9	4	<u>L18</u>
<u>L17</u>	l11 and l8	2	<u>L17</u>
<u>L16</u>	l11 and l7	2	<u>L16</u>
<u>L15</u>	l11 and l6	4	<u>L15</u>
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<u>L13</u>	(emulat\$3) and l4	39	<u>L13</u>
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<u>L11</u>	(emulat\$3) and l2	782	<u>L11</u>
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<u>L9</u>	(717/136-148)![CCLS]	3489	<u>L9</u>
<u>L8</u>	(716/3,16,17)[CCLS]	2103	<u>L8</u>
<u>L7</u>	(712/227,228)[CCLS]	1424	<u>L7</u>
<u>L6</u>	(712/2-17,21,22)[CCLS]	1913	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	14011	<u>L5</u>
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<u>L4</u>	L3 and (switch? or select?) near5 mode\$1	66	<u>L4</u>
<u>L3</u>	(pld or programmable or pla or field near1 programmable or fpga) and (translat\$6 or conversion or convert\$5) near10 native	2286	<u>L3</u>
<u>L2</u>	L1 and (switch? or select?) near5 mode\$1	4866	<u>L2</u>

L1 (pld or programmable or pla or field near1 programmable or fpga) and
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215259 L1

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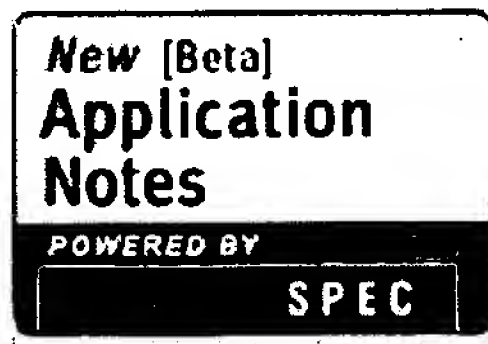
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